

35 USC §102

Claims 1-3 and 6 were rejected under 35 USC §102 in view of Taraci. In order to serve as a §102 reference, the reference must teach all the claimed elements.

Claim 1 is directed to a processor for executing image processing "being characterized in having programming means for implementing programmable stall clock cycles . . ." The patent specification describes an exemplary "means" for implementing the function as an accumulator (see Specification page 4 lines 21-28 and Fig. 4). The invention exemplary embodiment employs the carry output as the effective clock. This creates the programmable stall clock function.

Taraci is directed to a technique for synchronizing video inputs to a particular output. Taraci does not teach a programmable stall clock, but rather a synchronization technique. The Examiner cites a portion of the reference (col. 8 lines 35-50) and contends that Taraci describes a frame rate delay equivalent to the claimed programmable stall clock.

The Taraci reference is directed to very different problem than in the subject Application. Taraci intends to synchronize inputs and outputs, while the subject Application is directed to reducing video processor bus contention by applying a programmable stall clock. Consequently, Applicant submits that the Taraci structure and function is for video synchronization, not for implementing a programmable stall clock. Applicant strenuously disagrees that the Taraci frame rate delay is the same as the claimed programmable stall clock.

Accordingly, Applicant submits that the Taraci reference does not teach all the claimed elements, and therefore, cannot serve as a §102 reference to reject claims 1-3 and 6.

35 USC §103

Claims 4, 5 and 8 were rejected under §103 in view of Taraci and Crump. Claim 7 was rejected under §103 in view of Taraci and Kondoh.

As described above, Applicant submits that claim 1 is allowable over the Taraci reference. Accordingly, Applicant submits that the dependent claims 4, 5, 7 and 8 are also allowable.

The Taraci reference is directed to synchronizing video inputs and outputs, while the subject Application is directed to reducing video processor bus contention. Consequently, Applicant submits that the Taraci structure and function is for video synchronization, not for